Physics 330, Fall 2013, Course Schedule

Instructor: Dr. Matthew M. Waite, Merion 133, ext: 2573, mwaite@wcupa.edu

The best way to contact me is via e-mail. But, be aware that I will only read and respond to e-mails written in proper English, with correct pronunciation, grammar, spelling, and etiquette. Do not send me any e-mails addressed to "hey" written in text-speak like you're talking to your roommate about meeting up at Barnaby's tonight... I might meet up for happy hour, but I won't answer any questions about class or homework!

Class Meeting: Lecture M 2-5 (SSN 191); Lab M 3-5 or Th 1-3 (MER 118)

Office Hours: TBA, or by appointment.

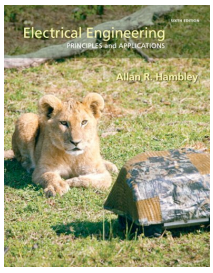
Course Web Page: D2L

Course information can be found here throughout the semester. The syllabus, homework problem solutions, and other interesting stuff can be found here. Check it periodically!!

The textbook publisher has a CourseSmart e-book version:

http://www.mypearsonstore.com/bookstore/electrical-engineering-principles-applications-9780133116649?xid=PSED

Text: Electrical Engineering, (6th Ed.) by Allan R. Hambley, Prentice Hall (New Jersey, 2013)



Grading:

3 tests (15% each)	45%
Cumulative Final	20%
Homework	15%
Laboratory	20%

Attendance:

You are expected to attend every class period. For unforeseen circumstances, each student will be allowed three (3) unexcused absences.

Excused absences are limited to University-Sanctioned Events (which follow the Excused Absence Policy for University-Sanctioned Events as described in the West Chester University Undergraduate Catalog), and absences due to serious illness or injury (verified by a practicing MD, you must provide me with a phone number), or the death of family members (also to be verified.)

Cell phones and texting are NOT ALLOWED during class. Cell phone use is disruptive to your classmates and to your instructors. Please turn off your phones or put them on **silence**, tuck them away in your bookbag, purse or European carry-all, and forget about them until after class. If I see anyone texting or using their cell phones during class, I will take 5 points off of the nearest exam grade (that's 5 full points out of 100), and you will be considered "absent" for that day, since you are obviously not mentally present.

Exams:

There will be three exams over the course of the semester. These will be given at alternative times during the week in order to allow for extra time. The first will be given after completion of chapters 1-3. This material covers many of the basics and a few new things. The second will be given after completion of chapters 4, 5, 6, and 10. The third will be given after completion of chapters 11, 14, 12 and 13. The final exam will be cumulative, and will include all the digital logic.

Laboratory:

Each Monday we will start with a bit of lecture and then segue into a laboratory exercise. We have 2-3 hours of class period on Monday afternoons, during which you are to get started on the experiment. I will be present during this time to answer any questions you might have and to make sure you get started correctly. If you do not finish the exercise Monday afternoon, you are to complete the lab exercise by the following Monday class period. Again, you may come ask me questions during my office hours throughout the week, but you are EXPECTED to stay on Monday afternoons. If you finish the lab early (which won't happen often) you may spend the rest of the time experimenting with the electronics simulation software.

University Policies:

For questions regarding Academic Dishonesty, the No-Grade Policy, Sexual Harassment, or the Student Code of Conduct, students are encouraged to refer to their major department's handbook, the Undergraduate Course Catalogue, the Rams Eye View, or the University Web Site. Please understand that improper conduct in any of these areas will not be tolerated and may result in immediate ejection from the class.

Disability:

We at West Chester University wish to make accommodations for persons with disabilities. Please make your needs known by contacting the Office of Services for Students with Disabilities at ext. 3217 **and** me at the above listed contact information. Sufficient notice is needed in order to make the accommodations possible. The University and I desire to comply with the ADA of 1990.

Miscellaneous:

Please make any restroom visits before the class starts, or wait until it ends, the class is only 50 minutes. It is distracting to both the instructor as well as your fellow students when someone gets up and walks out of the classroom during lecture. Only in very rare circumstances will permanent physical damage be done by waiting a few more minutes for class to end... In fact, many doctors claim that waiting up to 2-3 hours is no problem (except for a bit of discomfort) at all. If I go off on a tangent and babble on and on for 2-3 hours, I'll let you get up and take a break... promise.

Schedule: (This schedule is tentative, I will try to follow it as closely as possible!)

Month	Date	Reading	Topic	Hmwk	Problems
Aug.	26	Ch. 1	Intro. & Intro Lab (Intro. Equip. & Techniques)		
	28	Ch. 2	Resistive Circuits		
Sept.	2	Labor Day	No Classes		
	4	Ch. 2	Thevenin & Norton Circuits	1	12, 25, 37, 41, 50, 56, 65, 66, 77
	9	Ch. 3	Inductors & Capacitors (R's & Kirchoff's Laws Lab)		
	11	Ch. 3	Inductors & Capacitors		
	13	(Friday)		2	5, 6, 30, 36, 37, 39, 53, 68, 69, 83
	16	Ch. 4	Transients (Thevenin & Norton Circuits Lab)		
	18	Ch. 4	Transients		
	20	(Friday)		3	10, 26, 27, 36, 44, 49, 62, 67
	23	Ch. 5	Steady-State Sinusoidal Analysis (No Lab)		
	25	**1, 2, 3**	**EXAM I (Ch. 1, 2, 3)**		
	30	Ch. 5	Steady-State Sinusoidal Analysis (RC Circuits)		
Oct.	2	Ch. 6	Frequency Response, etc.	4	3, 11, 18, 26, 29, 38, 50, 54
	7	None	Fall BREAK		
	9	Ch. 6	Frequency & Bode Plots		
	14	Ch. 10	Diodes (LCR & Resonance)		
	16	Ch. 10	Diodes		
	18	(Friday)		5	4, 11, 24, 29, 42, 51, 53, 57, 88
	21	Ch. 11	Amplifiers (No Lab)		
	23	Ch. 14	Op. Amps.		
	25	(Friday)		6	3, 11, 26, 33, 51, 52, 65, 72, 82
	28	Ch. 14	Op. Amps. (Intro. to Op. Amp. Circuits)		
	30	**4, 5, 6, 10**	**EXAM II (Ch. 4, 5, 6, 10)**		
Nov.	4	Ch. 14	Op. Amps. (Op. Amp. Circuits-Integrators)		
	6	Ch. 14	Op. Amps.	11	5, 11, 13, 67, 74, 75
	11	Ch. 12	FET Transistors (Active Filters)		
	13	Ch. 13	BJT Transistors	14	1, 4, 9, 11, 20,22, 24, 33, 74, 78
	18	Ch. 13	BJT Transistors (No Lab)		
	20	Ch. 7	Digital Logic (Intro. to Digital Logic)		
	25	Ch. 7	Digital Logic	13	7, 9, 44, 51, 62
	27	None	Thanksgiving Break		
Dec.	2	Ch. 7	Digital Logic (Flip-Flops)		
	4	**11, 14, 12, 13**	**EXAM III (Ch. 11, 14, 12, 13)**		
	9	Ch. 7	Digital Logic (Finish up)	7	3, 12, 14, 29, 33, 37, 44, 56, 59, 75
	??	All of it!!	Final Exam		